

## WHAT IS CLAIMED IS:

Suba47 1. A video data transfer system comprising:

a real time output path through which video data processed by a video processor is sent to a display via a frame buffer; and

5 a capturing-only path which is independent of said real time output path and through which said video data is sent to a system memory via a system bus.

2. A video data transfer system as defined by claim 1, wherein said real time output path comprises:

an off-screen memory which receives video data from said video processor via a data bus and stores video data therein,  
5 said off-screen memory being in the frame buffer; and

a display control circuit which receives video data read from said off-screen memory via said data bus for enlargement and interpolation processing and transfers processed results to said display, and wherein

10 said capturing-only circuit comprises:

a gate which is opened only when video data is received from said video processor for capturing; and

memory means for storing said video data sent through said gate and for transferring said video data to said system bus.

3. A video data transfer system as defined in claim 2, wherein

said memory means transfers said stored video data to said system bus when said system bus is not occupied by some

09067721.042898  
868240.1279060

other unit and, when said system bus is occupied by some other  
5 unit, checks if said stored data contains a field delimiter or a  
frame delimiter and closes said gate to stop data transfer when  
said stored data contains the delimiter and, when said stored data  
does not contain the delimiter, stores the next video data passing  
through said gate.

Add a5 >

Add B9 >

Add C4 >

09067721.042898